

(19) 日本国特許庁 ( J P )

## (12) 公 開 特 許 公 報 ( A )

(11) 特許出願公開番号

特開2002-10505

( P2002-10505A )

(43) 公開日 平成14年1月11日 (2002.1.11)

(51) Int.Cl.<sup>7</sup>

識別記号

F I

データベース ( 参考 )

H 0 2 J 7/00

H 0 2 J 7/00

S 5 G 0 0 3

H 0 1 M 10/44

H 0 1 M 10/44

Q 5 H 0 3 0

10/48

10/48

P

審査請求 未請求 請求項の数 3 ○ L ( 全 9 頁 )

(21) 出願番号 特願2000-180827 ( P2000-180827 )

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(22) 出願日 平成12年6月16日 (2000.6.16)

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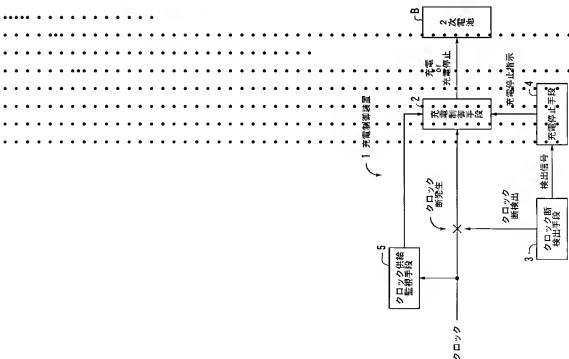
Fターム ( 参考 ) 5G003 AA01 BA01 CA03 CB01 CC02

FA04 GA01 GC01 CC05

5H030 AA03 AA06 AS11 AS18 B801

B327 DD04 FF41

(54) 【発明の名称】 充電制御装置



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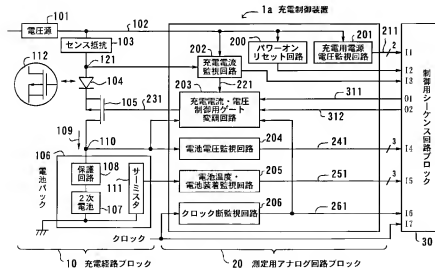
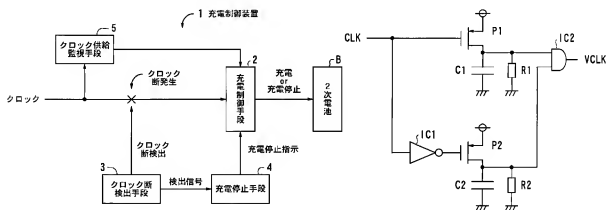
A large grid of dots with a few dots highlighted in black to form a sparse pattern. The pattern includes a vertical line of dots on the left, a horizontal line of dots near the top, and a small cluster of dots in the center. The dots are arranged in a regular grid, with the highlighted dots forming a sparse, irregular pattern.

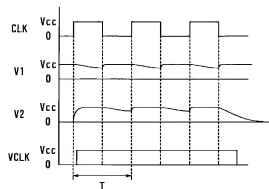
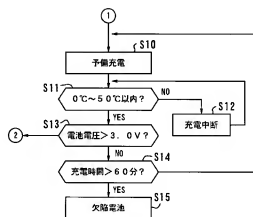
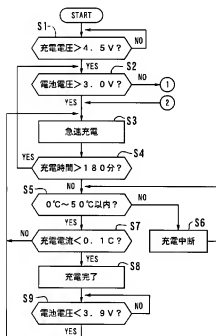
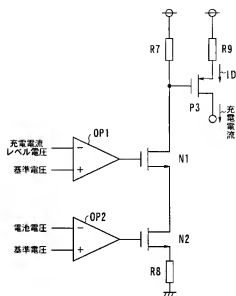
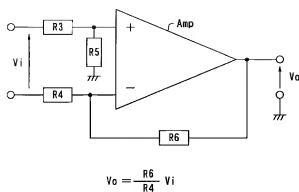
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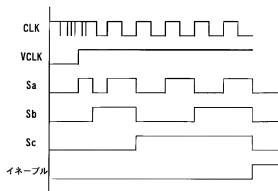
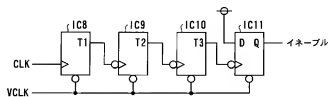
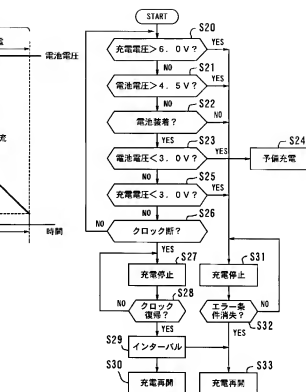
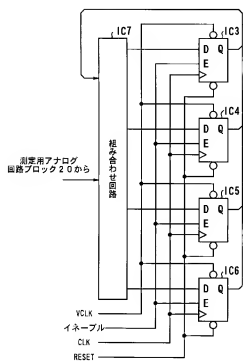
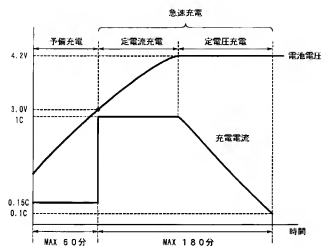
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# PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2002-010505

(43)Date of publication of application : 11.01.2002

(51)Int.Cl.

H02J 7/00

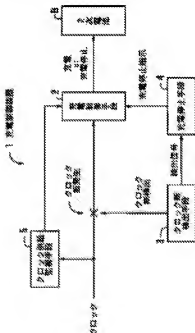
H01M 10/44

H01M 10/48

(21)Application number : 2000-180827 (71)Applicant : FUJII ELECTRIC CO LTD

(22)Date of filing : 16.06.2000 (72)Inventor : YOSHIDA YUTAKA

## (54) CHARGE CONTROLLER



(57)Abstract:

PROBLEM TO BE SOLVED: To prevent malfunctions in clock interruption for the enhancement of the safety and quality of charging operation.

SOLUTION: A charge controlling means 2 logically controls charging of a secondary battery B. A clock interruption detecting means 3 detects any clock interruption in clocks inputted to the charge controlling means 2 and generates a detection signal if any clock interruption occurs. A charge stopping means 4 stops charging operation based on the detection signal.